

WEST Search History

DATE: Friday, November 18, 2005

| Hide? | <u>Set</u> <u>Name</u> | <u>Query</u> | <u>Hit</u> <u>Count</u> |
|--------------------------|--|---|----------------------------|
| | <i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L45 | L44 and (L23 or L24 or L32) | 6 |
| <input type="checkbox"/> | L44 | 702/176.CCLS. | 308 |
| <input type="checkbox"/> | L43 | L39 and (L23 or L24 or L32) | 5 |
| <input type="checkbox"/> | L42 | L38 and (L23 or L24 or L32) | 2 |
| <input type="checkbox"/> | L41 | L37 and (L23 or L24 or L32) | 5 |
| <input type="checkbox"/> | L40 | (324/600).ccls. | 104 |
| <input type="checkbox"/> | L39 | (716/4).ccls. | 1976 |
| <input type="checkbox"/> | L38 | (702/107).ccls. | 260 |
| <input type="checkbox"/> | L37 | (702/96).ccls. | 87 |
| | <i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L36 | ('5014226' '5625803' '6292015' '6483165')![pn] | 4 |
| | <i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L35 | 6601177.uref. | 0 |
| <input type="checkbox"/> | L34 | 6601177.pn. | 1 |
| <input type="checkbox"/> | L33 | L2 and L32 | 4 |
| <input type="checkbox"/> | L32 | detect\$3 with voltage with speed | 7764 |
| | <i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L31 | (processor or processing or execution) with L30 | 209 |
| <input type="checkbox"/> | L30 | detect\$3 with voltage with speed | 6844 |
| <input type="checkbox"/> | L29 | L24 and L28 | 0 |
| <input type="checkbox"/> | L28 | optimiz\$5 with voltage with speed | 76 |
| <input type="checkbox"/> | L27 | L25 not L26 | 94 |
| <input type="checkbox"/> | L26 | measur\$5 with rate with (processor or processing or execution) with voltage | 73 |
| <input type="checkbox"/> | L25 | measur\$5 with speed with (processor or processing or execution) with voltage | 99 |
| <input type="checkbox"/> | L24 | measur\$5 with speed with (processor or processing or execution) | 2730 |
| <input type="checkbox"/> | L23 | measur\$5 with rate with (processor or processing or execution) | 2102 |
| | <i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i> | | |
| <input type="checkbox"/> | L22 | L21 not L20 | 5 |
| <input type="checkbox"/> | L21 | L16 and L19 | 7 |

| | | | |
|--------------------------|-----|--|-------|
| <input type="checkbox"/> | L20 | L8 and L19 | 7 |
| <input type="checkbox"/> | L19 | measur\$5 with rate with (processor or processing or execution) | 5560 |
| <input type="checkbox"/> | L18 | L17 not (L15 or L14) | 11 |
| <input type="checkbox"/> | L17 | L13 and L16 | 12 |
| <input type="checkbox"/> | L16 | (713/300).ccls. | 1866 |
| <input type="checkbox"/> | L15 | L14 not L10 | 4 |
| <input type="checkbox"/> | L14 | L8 and L13 | 6 |
| <input type="checkbox"/> | L13 | measur\$5 with speed with (processor or processing or execution) | 5121 |
| <input type="checkbox"/> | L12 | 6014749.uref. | 0 |
| <input type="checkbox"/> | L11 | 6014749.pn. | 1 |
| <input type="checkbox"/> | L10 | measur\$5 with speed and L9 | 8 |
| <input type="checkbox"/> | L9 | speed with voltage and L8 | 77 |
| <input type="checkbox"/> | L8 | (713/320).ccls. | 1063 |
| <input type="checkbox"/> | L7 | (712/320).ccls. | 0 |
| <input type="checkbox"/> | L6 | L2 and L4 | 21 |
| <input type="checkbox"/> | L5 | L1 and L4 | 552 |
| <input type="checkbox"/> | L4 | optimiz\$5 with voltage | 10993 |
| <input type="checkbox"/> | L3 | L1 and L2 | 1 |
| <input type="checkbox"/> | L2 | (713/320).ccls. | 1063 |
| <input type="checkbox"/> | L1 | optimiz\$5 with voltage with speed | 552 |

END OF SEARCH HISTORY